Voltage Stability Improvement Adopting Hybrid AC Microgrid on the Distribution Network: Iraq as a Case Study

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ABSTRACT

This paper, a microgrids was analyzed, designed and simulated for Holy Karbala Distribution Network by using (MATLAB 2014 a) program. Two criteria are studies in this paper to show the effect of applying a microgrid to the voltage stability of the system; these indexes are the L-index and Fast Voltage Stability Index (FVSI).

INTRODUCTION

Microgrids (MG) are small grouping of interconnected power generation and control technologies that can operate within or independent of a central grid and can satisfy customer requirements, such as local reliability enhancement, feeder loss reduction, local voltage regulation and increasing efficiency through the use of waste heat [1].

Holy Karbala Distribution Network:

Holy Karbala distribution network is connected to the Iraqi power grid at Al-Mussayab bus bar to the southeast of Holy Karbala city, there is another connection at Baghdad bus bar. Holy Karbala distribution network consists of six transformers substations transform the high voltage (132 KV) to the medium voltage (33 and 11 KV). They are shown as follows:

- Three substations each substation contains three transformers of 132/33/11 KV.
- Three substations each substation contains two transformers of 132/33 KV.
- Eleven substations each substation contains two transformers of 33/11 KV.
- One substation contains three transformers of 33/11 KV.

These substations are shown diagrammatically in Figure (1), the bus data and line data of system are given in Appendix A.
**Bus Voltage Stability by L-Index:**

**Generalization to a multi-bus system [2]:**

From the basic theory of multi-bus power system, all the buses can be divided into two categories: Generator bus (PV bus and Slack bus) and Load bus (PQ bus). Because the voltage stability problem is reactive power relative problem, and the generator bus can provide the reactive power to support the voltage magnitude of the bus, it is absolutely necessary that buses should be distinguished.

We can use the Kirchhoff Current Law to express the circuit of N-bus system:

\[
\begin{bmatrix}
I_L \\
I_G
\end{bmatrix} =
\begin{bmatrix}
Y_{LL} & Y_{LG} \\
Y_{GL} & Y_{GG}
\end{bmatrix}
\begin{bmatrix}
V_L \\
V_G
\end{bmatrix}
\]

Here the subscripts L and G denote load buses and generator buses, respectively.

Equation (1) can be expressed in the hybrid form:

\[
\begin{bmatrix}
V_L \\
I_G
\end{bmatrix} =
\begin{bmatrix}
Z_{LL} & K_{LG} \\
F_{GL} & M_{GG}
\end{bmatrix}
\begin{bmatrix}
I_L \\
V_G
\end{bmatrix}
\]

(2)

Where \(Z_{LL}, K_{LG}, F_{GL}, M_{GG}\) are Sub matrices of the hybrid matrix (H-matrix)

Actually, the sub matrix \(Z_{LL}\) or \(K_{LG}\) is the most important part in the L index derivation.

where:

\[
Z_{LL} = Y_{LL}^{-1}
\]

When we consider the voltage at load bus \(j\), we use the following equation:

\[
\overline{V}_j = \sum_{i \in L} \overline{Z}_{ji} I_i + \sum_{k \in G} \overline{R}_{jk} \overline{V}_k
\]

Which can be expressed as follows:

\[
\overline{V}_j = \sum_{k \in G} \overline{R}_{jk} \overline{V}_k = \sum_{i \in L} \overline{Z}_{ji} I_i
\]

Multiplying by \(V_j^*\); at both sides of the above equation:

\[
V_j^2 + V_j^* \overline{V}_{oj} = V_j^* \sum_{i \in L} \overline{Z}_{ji} I_i
\]

(3)

Where:

\[
\overline{V}_{oj} = - \sum_{k \in G} \overline{R}_{jk} \overline{V}_k
\]

The term \(\overline{V}_{oj}\) is representative of an equivalent generator comprising the contribution from all generators.

Expanding the \(j\) term in equation (3), we get:

\[
V_j^2 + V_j^* \overline{V}_{oj} = V_j^* (\overline{Z}_{jj} I_j + \sum_{i \neq j} \overline{Z}_{ij} S_i^* V_i)
\]

\[
V_j^2 + V_j^* \overline{V}_{oj} = V_j^* I_j \overline{Z}_{jj} + \sum_{i \neq j} \overline{Z}_{ij} S_i^* V_i
\]

\[
V_j^2 + V_j^* \overline{V}_{oj} = S_j \overline{Z}_{jj} + \sum_{i \neq j} \overline{Z}_{ij} S_i^* V_i
\]
\[ V_j^2 + V_j^* \overline{V}_{oi} = \overline{Z}_{ii} (S_j^* + \sum_{i \in L} \overline{Z}_{ij} S_i^* V_i^*) \]
\[ V_j^2 + V_j^* \overline{V}_{oi} = \overline{Z}_{ii} (S_j^* + S_j^+) \]
\[ V_j^2 + V_j^* \overline{V}_{oi} = \overline{Z}_{ii} S_j^+ \]  
(4)

where

\[ S_j^+ = \sum_{i \in L} \overline{Z}_{ij} S_i^* V_i^* \]

\( S_j^+ \) represents the 'contributions' of the other load buses, while \( S_j^* \) represents the load at bus j itself.

\( \overline{Z}_{ii} \): the element of sub matrix \( Z_{LL} \) at the ith load bus that is connected to the load bus j.

\( Z_{ij} \): the element of sub matrix \( Z_{LL} \) at the load bus j.

Hence, rearranging equation (4), we get:

\[ V_j^2 + V_j^* \overline{V}_{oi} = \frac{S_{j}^+}{V_j^*} \]  
(5)

where

\( Y_{jj} = \overline{Z}_{jj}^{-1} \)

From the previous analysis, we know that:

\[ L_j = \left[ 1 + \frac{V_{oi}}{V_j} \right] = \frac{S_{j}^+}{V_j^* Y_{jj}} = \frac{S_{j}^+}{V_j^* * Y_{jj}} \]  
(6)

From equation (4), it is clear that the L index of the voltage stability at a load bus is mainly influenced by the equivalent load \( S_{j}^+ \), which has two parts: the load at bus j itself \( S_j^* \), and the 'contributions' of the other load buses \( S_{j}^+ \). When the load at a load bus changes, it will influence the L index.

For the system, the vulnerable load bus must have the maximum L index value.

Index system = Max(Index j) , where j ∈ L

From the derivation, it is seen that for computing the L index, at any load bus, one requires only the knowledge of the power flow information and the network topology, which can be obtained very quickly in real-time.

The procedure for voltage stability L index programming is summarized below:

1. Initialize active and reactive load power of the load bus which has to be tested, where two variables are used for this purpose, such as \( P_L \) and \( Q_L \).
2. Define power system base MVA, power mismatch accuracy, acceleration factor and maximum number of iterations.
3. Enter bus data, line data of the electrical network.
4. The load flow solution is performed using NR power flow.
5. The resulting complex load bus voltages for all load buses are saved.
6. If the iterative solution converges, the active and reactive load power are increased with constant power factor. The increment can be expressed as \( \Delta P_L \) for active power and \( \Delta Q_L \) for reactive power. Then, steps 3 and 4 are repeated until the iterative solution diverges.
7. The sub-matrix \( Z_{LL} \) is formed by inverting the sub-matrix \( Y_{LL} \). Y_{LL} represents the admittances that connect the load buses with each other, which can be obtained easily from the Y-matrix of the network.
8. Extract the terms corresponding to the specified load bus (bus j) from the sub-matrix \( Z_{LL} \). Then, calculate \( S_{j}^+ \) from the following equation:

\[ S_{j}^+ = S_j^* + \sum_{i \in L} \overline{Z}_{ij} S_i^* V_i^* \]  
(7)

9. The L index of (bus j) is calculated from the following equation:

\[ L_j = \frac{S_{j}^+}{V_j^* * Y_{jj}} \]  
(8)

**Fast Voltage Stability Index (FVSI)[3]:**

This index can either be referred to a bus or a line. The voltage stability index developed in this work is referred to a line. Generally, it is started with the current equation to form the power or voltage quadratic equations.

The criterion employed in this work was to set the discriminant of the roots of voltage or power quadratic equation to be greater than zero.
When the discriminant is less than zero, it causes the roots of the quadratic equations to be imaginary which in turn cause voltage instability in the system. The line index that is evaluated close to 1 will indicate the limit of voltage instability.

\[
V_1, V_2 = \text{Voltage on sending and receiving buses} \\
P_1, Q_1 = \text{active and reactive power on the sending bus} \\
P_2, Q_2 = \text{active and reactive power on the receiving bus} \\
S_1, S_2 = \text{apparent power on the sending and receiving buses} \\
\delta = \delta_1 - \delta_2 \quad (\text{angle difference between sending and receiving buses})
\]

The line impedance is noted as \( Z = R + jX \) with the current (I) that flows in the line given by:

\[
I = \frac{V_1 \angle \theta - V_2 \angle \delta}{R + jX} \quad (9)
\]

\( V_1 \) is taken as the reference and therefore the angle is shifted to 0.

The apparent power at bus 2 can be written as:

\[
S_2^* = V_2^* I^* \quad (10)
\]

Rearranging the Equation (10) yields:

\[
I = \left(\frac{S_2}{V_2}\right)^* \quad (11)
\]

Equating Equations (9) and (12),

\[
V_1 \angle \theta - V_2 \angle \delta = \frac{P_2 - jQ_2}{V_z \angle \delta} \\
V_1 V_2 \angle \delta - V_2^2 = (R + jX)(P_2 - jQ_2) \quad (13)
\]

Separating the real and imaginary parts yields,

\[
V_1 V_2 \cos \delta - V_2^2 = RP_2 + XQ_2 \quad (14)
\]

And,

\[
-V_1 V_2 \sin \delta - V_2^2 = XP_2 - RQ_2 \quad (15)
\]

Rearranging Equation (14) for \( P_2 \) and substituting into equation (13) yields a quadratic equation of \( V_2 \):

\[
V_2^2 - \left(\frac{P_2 \sin \delta + \cos \delta}{X}\right) V_2 + \left(\frac{X + R^2}{X}\right) Q_2 = 0 \quad (16)
\]

The roots for \( V_2 \) will be:

\[
V_2 = \frac{R \sin \delta + \cos \delta}{2} \left[ \frac{\sqrt{R^2 \sin \delta + \cos \delta V_1^2}}{2} - 4\left(\frac{X + R^2}{X}\right) Q_2 \right] \quad (17)
\]

To obtain real roots for \( V_2 \), the discriminant is set greater than or equal to 0:

\[
\left(\frac{R}{X} \sin \delta + \cos \delta V_1\right)^2 - 4\left(\frac{X + R^2}{X}\right) Q_2 \geq 0 \quad (18)
\]

Since \( \delta \) is normally very small then

\[
\delta \approx 0 \quad , R \sin \delta \approx 0 \text{ and } X \cos \delta \approx X
\]

Taking the symbols ‘i’ as the sending bus and ‘j’ as the receiving bus, the Fast Voltage Stability Index (FVSI) can be written as follows:

\[
\text{FVSI}_{ij} = \frac{4Q_j}{V_i^2 X_{ij}} \quad (19)
\]

Where,

- \( Z \) = line impedance
- \( X_{ij} \) = line reactance
Q_r = reactive power at the receiving end  
V_r = sending end voltage

The value of FVSI that is evaluated close to 1 indicates that the particular line is close to its instability point which may lead to voltage collapse in the entire system. To maintain a secure condition the value of FVSI should be maintained well less than 1.

5. Voltage Stability Improvement Results:

Two criteria adopted in this work for voltage stability improvement as below:

A. Bus bar voltage stability:

L- Index is adopted to show the voltage stability improvement on the bus bar. Table (1) shows the effect of adopting microgrid strategy on the bus bar stability.

<table>
<thead>
<tr>
<th>Substation</th>
<th>L-Index without MG</th>
<th>L-Index with MG</th>
</tr>
</thead>
<tbody>
<tr>
<td>Al-Hindia</td>
<td>0.0348</td>
<td>0.00777</td>
</tr>
<tr>
<td>Al-Hussainia</td>
<td>0.372</td>
<td>0.14515</td>
</tr>
</tbody>
</table>

It's clear that using of microgrids lead to decrease L-Index which indicates that it increase the voltage stability on the bus bar.

B. Line voltage stability:

Fast Voltage Stability Index (FVSI) is adopted to show line voltage stability improvement. Table (2) shows the effect of adopting microgrid strategy on the line stability.

<table>
<thead>
<tr>
<th>Line no.</th>
<th>Line location</th>
<th>FVSI without MG</th>
<th>FVSI with MG</th>
</tr>
</thead>
<tbody>
<tr>
<td>15</td>
<td>Al-Hindia mobile-Al-Hindia SS</td>
<td>0.1463</td>
<td>0.0699</td>
</tr>
<tr>
<td>21</td>
<td>East Karbala-Al-Hussainia SS</td>
<td>0.7364</td>
<td>0.4717</td>
</tr>
</tbody>
</table>

From table (2), it's clear that using of microgrids lead to decrease FVSI index which indicates that it increase the voltage stability on the line.

Conclusions:

From the results above its clear that using of microgrids lead to decrease L-Index which indicates that it increase the voltage stability on the bus bar and also it lead to decrease FVSI index which indicates that it increase the voltage stability on the line.

REFERENCES


Appendix A:

Data Base of Holy Karbala distribution system

<table>
<thead>
<tr>
<th>Bus NO.</th>
<th>Station Name</th>
<th>V P.U.</th>
<th>δ Degree</th>
<th>P_L MW</th>
<th>Q_L Mvar</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>East Karbala</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>North Karbala</td>
<td>-</td>
<td>-</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>3</td>
<td>South Karbala</td>
<td>-</td>
<td>-</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>4</td>
<td>Al-Tahaddy mobile</td>
<td>-</td>
<td>-</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>Al-Salam mobile</td>
<td>-</td>
<td>-</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>
### Table A.2: Line data of Holy Karbala distribution system

<table>
<thead>
<tr>
<th>Line No.</th>
<th>From Bus</th>
<th>To Bus</th>
<th>R (P.U)</th>
<th>X (P.U)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1</td>
<td>7</td>
<td>1.205</td>
<td>3.146</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
<td>7</td>
<td>1.405</td>
<td>3.668</td>
</tr>
<tr>
<td>3</td>
<td>2</td>
<td>8</td>
<td>1.147</td>
<td>2.996</td>
</tr>
<tr>
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<td>2</td>
<td>8</td>
<td>0.557</td>
<td>1.455</td>
</tr>
<tr>
<td>5</td>
<td>2</td>
<td>9</td>
<td>0.451</td>
<td>1.178</td>
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<tr>
<td>6</td>
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<td>9</td>
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</tr>
<tr>
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<td>1.138</td>
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<tr>
<td>8</td>
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<td>9</td>
<td>3</td>
<td>11</td>
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<td>0.511</td>
</tr>
<tr>
<td>10</td>
<td>3</td>
<td>11</td>
<td>0.195</td>
<td>0.510</td>
</tr>
<tr>
<td>11</td>
<td>3</td>
<td>12</td>
<td>0.354</td>
<td>0.925</td>
</tr>
<tr>
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<td>3</td>
<td>12</td>
<td>0.355</td>
<td>0.927</td>
</tr>
<tr>
<td>13</td>
<td>4</td>
<td>13</td>
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<td>0.340</td>
</tr>
<tr>
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<td>4</td>
<td>13</td>
<td>0.131</td>
<td>0.341</td>
</tr>
<tr>
<td>15</td>
<td>6</td>
<td>14</td>
<td>0.058</td>
<td>0.153</td>
</tr>
<tr>
<td>16</td>
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<td>15</td>
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<td>0.683</td>
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<td>17</td>
<td>5</td>
<td>16</td>
<td>0.503</td>
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</tr>
<tr>
<td>18</td>
<td>3</td>
<td>16</td>
<td>0.505</td>
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</tr>
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<td>17</td>
<td>0.212</td>
<td>0.554</td>
</tr>
<tr>
<td>20</td>
<td>2</td>
<td>17</td>
<td>0.212</td>
<td>0.554</td>
</tr>
<tr>
<td>21</td>
<td>1</td>
<td>18</td>
<td>1.175</td>
<td>3.066</td>
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